

Search

About 260 results (0.23 seconds)

Everything

Filing date: oldest

Images

Maps

Videos

News

Shopping

Patents

More

Any date

Restrict by filing date

Restrict by issue date

Any filing status

Applications

Issued patents

Point of sale terminal having prompting display and automatic...

www.google.com/patents/US4310686
 US Pat. 4310686 - Filed Nov 8, 1978 - Issued Jan 12, 1982 - Auto-Register, Inc.
 ... 58 above the group numeric keys can be used to enter the sale of gift coupons. ... enter his or her three digit attendant **number** on the numeric pad 54, ...

Overview - Abstract - Drawing - Description - Claims

Promotional system with magnetic stripe and visual...

www.google.com/patents/US5932869
 US Pat. 5932869 - Filed Dec 27, 1996 - Issued Aug 3, 1999 - Graphic Technology, Inc.
 A unique user identification **number** is assigned to that user and encoded on the corresponding user **card** 10 and correlated with the file containing that ...

Overview - Abstract - Drawing - Description - Claims

Method and apparatus for selling subscriptions to periodicals in a...

www.google.com/patents/US5926796
 US Pat. 5926796 - Filed May 5, 1997 - Issued Jul 20, 1999 - Walker Asset Management Limited Partnership
 Upon receipt of the **activation card** from the consumer and the payment from ... **point-of-sale** and then sending the pre-paid subscription **activation card** ...

Overview - Abstract - Drawing - Description - Claims

Intelligent agent for executing delegated tasks

www.google.com/patents/US5932900
 US Pat. 5932900 - Filed Oct 6, 1997 - Issued Nov 9, 1999
 It can also learn the user's preferences for gift items such as flowers (eg, ... automatic electronic verification) by pre-entered credit **card** account, ...

Overview - Abstract - Drawing - Description - Claims

Promotion system including an ic-card memory for obtaining and...

www.google.com/patents/US6024286
 US Pat. 6024286 - Filed Dec 24, 1997 - Issued Feb 15, 2000 - Graphic Technology, Inc.
 A unique user identification **number** is assigned to that user and encoded on the corresponding user **card** 10 and 25 correlated with the file containing that ...

Overview - Abstract - Drawing - Description - Claims

List view

Grid view

Sorted by relevance

Filing date: latest

Filing date: oldest

Reset tools

System for calculating occasion dates and converting between...

www.google.com/patents/US6108640
 US Pat. 6108640 - Filed Jan 13, 1998 - Issued Aug 22, 2000
 It can also learn the user's preferences for gift items such as flowers ... (after automatic electronic verification) by pre-entered credit **card** account, ...

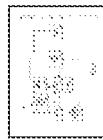
Overview - Abstract - Drawing - Description - Claims

Method and apparatus for selling subscriptions to periodicals in a...

www.google.com/patents/US6334113
 US Pat. 6334113 - Filed May 5, 1996 - Issued Dec 26, 2001 - Walker Digital, LLC
 The consumer could purchase a box containing a **gift card** to be sent to the ... **point-of-sale** and then sending the pre-paid subscription **activation card**

to ...
[Overview](#) - [Abstract](#) - [Drawing](#) - [Description](#) - [Claims](#)

[Method and apparatus for selling subscriptions to periodicals in a...](#)



www.google.com/patents/US6542874
US Pat. 6542874 - Filed May 5, 1998 - Issued Apr 1, 2003 - Walker Digital, LLC
The consumer could purchase a box containing a **gift card** to be sent to the
... **point-of-sale** and then sending the pre-paid subscription **activation** card
to ...

[Overview](#) - [Abstract](#) - [Drawing](#) - [Description](#) - [Claims](#)

[Method and apparatus for wireless assistance for self-service checkout](#)



www.google.com/patents/US6497362
US Pat. 6497362 - Filed Feb 15, 2001 - Issued Dec 24, 2002 - New Check
Corporation
Thus, supervisory personnel can intervene to process miscellaneous tender
such as, bottle refunds, **gift certificates**, rain checks, paper food stamps and
WIC ...

[Overview](#) - [Abstract](#) - [Drawing](#) - [Description](#) - [Claims](#)

[Scheme for promoting purchases by reminding purchases through...](#)



www.google.com/patents/US20020065693
US Pat. App. 9904497 - Filed Jul 16, 2001 - KABUSHIKI KAISHA TOSHIBA
Then, the **POS** register C carries out the **calculation** processing, ... Namely,
in the **POS** register C, when the **IC card** reader/writer management unit C5 ...

[Overview](#) - [Abstract](#) - [Drawing](#) - [Description](#) - [Claims](#)

Gooooooooogle >

[1](#) [2](#) [3](#) [4](#) [5](#) [6](#) [7](#) [8](#) [9](#) [10](#) [Next](#)

[Advanced search](#) [Search Help](#) [Give us feedback](#)

[Google Home](#) [Advertising Programs](#) [Business Solutions](#) [Privacy & Terms](#)
[About Google](#)

line 7 of the claim, after “a memory in”, delete “electrical connection to”.

Regarding **claim 7**, on page 3, cancel this claim.

Regarding **claim 10**, on page ,3 cancel this claim.

Regarding **claim 33**, on page 7, line 6 of the claim, after “converting said speech from an analog format to”, replace “a digital signal” with --an audio digital signal--;

line 8 of the claim, after “processor is”, replace “included” with --co-located with a CPU--;

line 11 of the claim, after “residing in a memory”, insert --of said digital processor--;

after line 12, insert new lines of content: --loading an appropriate vocabulary into said speech engine in said of memory of said digital signal processor, depending on the context of operation being performed by a user--;

line 14 of the claim, after “electrical connection to said digital signal processor and”, delete the words “said memory of”;

line 16 of the claim, after “connection to said digital signal processor and”, delete the words “said memory of”.

Regarding **claim 34**, on page 7, cancel this claim.

-----End of Examiner's Amendment-----

Allowable Subject Matter

3. Claims 1, 4-5, 8-9, 11-19, 21, 23-25, 31 and 33 are allowed.

The following is an examiner's statement of reasons for allowance:

Regarding independent **claims 1 and 33**, the instant application is directed to a computer motherboard architecture and the associated method of processing speech. The independent claims, combining certain well-known features in the art, identifies the uniquely distinct features of comprising:

a DSP chip in the audio input data path, wherein the DSP chip is co-located with a CPU on the computer motherboard; a memory in the DSP; a command and control speech engine residing in said memory of said DSP chip; wherein the speech engine includes a vocabulary of speech terms enabled to be loaded into said memory which are associated with specific instructions or contextual environments; and wherein the DSP serves as the preprocessor of all speech input prior to execution of instructions by the CPU to process the speech input and is enabled to be dynamically set by a user in either a continuous speech mode or a command and control mode.

The prior art of record, Lambrecht et al. (US 5,951,664), Simar, Jr. et al (US 6,182,203 B1) and Hansen et al (US 5,640,490), provided numerous teachings and approaches of computer architectures having audio/speech processing capability and real time multimedia applications, including speech recognition and synthesis, providing multimedia bus, interface, various bridge logics and multimedia devices dynamically programmed by a central controller (CPU) on motherboard; using separate DSP having a memory for speech recognition, providing separate

lexical access processor; and applying speech recognition feature to command mode or to other applications, such as word-processor document. However, the combined features stated above, are not anticipated by, nor made obvious over the prior art of the record.

4. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

5. Please address mail to be delivered by the United States Postal Service (USPS) as follows:

Mail Stop _____
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

or faxed to: 571-273-8300, (for formal communications intended for entry)
Or: 571-273-8300, (for informal or draft communications, and please label
"PROPOSED" or "DRAFT")

If no Mail Stop is indicated below, the line beginning Mail Stop should be omitted from the address.

Effective January 14, 2005, except correspondence for Maintenance Fee payments, Deposit Account Replenishments (see 1.25(c)(4)), and Licensing and Review (see 37 CFR 5.1(c) and 5.2(c)), please address correspondence to be delivered by other delivery services (Federal Express (Fed Ex), UPS, DHL, Laser, Action, Purolater, etc.) as follows:

U.S. Patent and Trademark Office
Customer Window, Mail Stop _____
Randolph Building
Alexandria, VA 22314

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Qi Han whose telephone numbers is (571) 272-7604. The examiner can normally be reached on Monday through Thursday from 9:00 a.m. to 7:00 p.m. If

Art Unit: 2626

attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Richemond Dorvil, can be reached on (571) 272-7602.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Inquiries regarding the status of submissions relating to an application or questions on the Private PAIR system should be directed to the Electronic Business Center (EBC) at 866-217-9197 (toll-free) or 703-305-3028 between the hours of 6 a.m. and midnight Monday through Friday EST, or by e-mail at: ebc@uspto.gov. For general information about the PAIR system, see <http://pair-direct.uspto.gov>.

QH/qh
September 22, 2006



RICHEMOND DORVIL
SUPERVISORY PATENT EXAMINER

XYBERNAUT CORPORATION, 5175 Parkstone Drive, Suite 130, Chantilly VA 20151
Phone (703) 674-4861 / Fax (703) 480-0493



xybernaut

Technology that Works with You

FAX

To: Examiner Han

From: Christopher M. Tucker

Fax: 571 273 7604

Pages: (including cover) 7

Phone: 571 272 7604

Date: 9/21/2006

Re: Proposed claim amendments for 09/677,569

CC:

Urgent

For Review

Please Comment

Please Reply

Please Recycle

● Comments:

Dear Examiner Han,

Please see the attached.

Regards,

Christopher M. Tucker

CONFIDENTIALITY NOTE: This communication contains information that is confidential and/or legally privileged. This information is intended only for the use of the individual or entity named on this communication. If you are not the intended recipient, you are hereby notified that any disclosure, copying, distribution, printing or other use of, or any action in reliance on, the contents of this communication is strictly prohibited. If you receive this communication in error, please immediately notify us by telephone at (703) 631-6925.

Examiner Han,

Re: U.S. Patent Application No. 09/677,569

With regard to our telephone conversation of today (September 21, 2006) concerning proposed claim amendments for the above-identified U.S. patent application, please see the following claim changes as previously discussed.

INFORMAL AMENDENT

Claim 1 (presently amended) A computer motherboard architecture comprising:
a computer motherboard possessing typical components including a CPU, a data bus, a power interface, and an audio input data pathway, said audio input data pathway connecting the audio input of the motherboard to the CPU;
a DSP chip in the audio input data path, wherein the DSP chip is co-located with the CPU on the motherboard;
a bridge interfacing between said DSP chip and the bus on the computer motherboard;
a memory in [electrical-connection-to] said DSP chip;
a command and control speech engine residing in said memory of said DSP chip;
wherein said DSP is enabled to operate in either command and control mode or continuous speech mode and said DSP serves as the preprocessor of all speech input prior to execution of instructions by the CPU to process the speech input and wherein said speech engine includes a vocabulary of speech terms enabled to be loaded into said memory which are associated with specific instructions or contextual environments, and

Informal Amendment
U.S. Application No. 09/677,569

further wherein said DSP is enabled to be dynamically set by a user in either a continuous speech mode or a command and control mode.

Claims 2-3 (canceled)

Claim 4 (previously presented) A computer motherboard architecture according to claim 1 wherein said audio input data pathway comprises a microphone input, means for digitizing an audio input data pathway, and a DSP chip, bridge chip communicating with said bus.

Claim 5 (previously presented) A computer motherboard architecture according to claim 1 wherein said DSP chip is operable to convert said audio input into phonemes.

Claim 6-7(canceled)

Claim 8 (previously presented) A computer motherboard architecture according to claim 1 wherein said vocabulary of speech terms is able to be defined by a user, either in a static or active mode.

Claim 9 (previously presented) A computer motherboard architecture according to claim 1 wherein said vocabulary of speech terms is refreshed by the CPU based upon the context of an application running on a host processor.

Informal Amendment
U.S. Application No. 09/677,569
Claim 10 (canceled)

Claim 11 (previously presented) A computer motherboard architecture according to claim 1 wherein said DSP chip is operable to perform menu selection including mobile phone audio functions comprising voice activated dialing, voice control, noise cancellation, and speech to signal conversion.

Claim 12 (previously presented) A computer motherboard architecture according to claim 1 wherein said DSP chip is enabled to perform noise cancellation functions.

Claim 13 (previously presented) A computer motherboard architecture according to claim 1 wherein said DSP chip is enabled to function in a command and control speech mode.

Claim 14 (previously presented) A computer motherboard architecture according to claim 1 wherein said DSP chip is enabled to function in a continuous speech mode.

Claim 15 (previously presented) A computer motherboard architecture according to claim 1 wherein said DSP chip is enabled to function in a mobile phone mode.

Claim 16 (previously presented) A computer motherboard architecture according to claim 1 wherein said DSP is enabled to function in a language translation mode.

Informal Amendment
U.S. Application No. 09/677,569

Claim 17 (previously presented) A computer motherboard architecture according to
claim 1 wherein said computer motherboard is a user-supported computer motherboard.

Claim 18 (previously presented) A computer motherboard architecture according to
claim 17 wherein said user-supported computer is a voice activated user-supported
computer.

Claim 19 (previously presented) A computer motherboard architecture according to
claim 1 wherein said computer motherboard is a portable computer motherboard.

Claim 20 (canceled)

Claim 21 (previously presented) A computer motherboard architecture according to
claim 1 wherein said computer motherboard is a desktop computer motherboard.

Claim 22 (canceled)

Claim 23 (previously presented) A computer motherboard architecture according to
claim 1 wherein said computer motherboard is a video gaming system computer
motherboard.

Informal Amendment
U.S. Application No. 09/677,569

Claim 24 (previously presented) A computer motherboard architecture according to claim 1 wherein said computer motherboard is a computing and communications device computer motherboard.

Claim 25 (previously presented) A computer motherboard architecture of claim 1 wherein said computer motherboard is a component of a member selected from the group consisting of user supported computers, laptop computer, desktop computers, portable computers and mixtures thereof.

Claim 26-30 (canceled)

Claim 31 (previously presented) A computer motherboard architecture according to claim 1 wherein when said DSP is operating in command and control mode said DSP is operable to accommodate full interpreting and processing of said speech without said CPU being utilized.

Claim 32 (canceled)

Claim 33 (presently amended) A method of processing speech, the method comprising the steps of:

setting a computer in either command and control mode or continuous speech mode, inputting speech into an audio input device wherein said audio input device is electrically connected to said computer,

Informal Amendment
U.S. Application No. 09/677,569

converting said speech from an analog format to an audio digital signal,
transmitting said digital signal to a digital signal processor, wherein said digital signal
processor is [included] co-located with a CPU on a motherboard of said computer and
said digital signal processor is enabled to function as a preprocessor of all speech input,
analyzing said digital signal with at least said digital signal processor and a speech engine
residing in a memory of said digital signal processor on said motherboard and electrically
connected to said digital signal processor,
34 loading an appropriate vocabulary into said speech engine in
said memory of said digital signal processor
including speech-engine depending on the context of the operation being performed by a
user;

transmitting said analyzed digital signal of a computer command to a processor in
electrical connection to said digital signal processor and [said memory of] said computer,
transmitting said analyzed digital signal of continuous speech to a processor in electrical
connection to said digital signal processor and [said memory of] said computer,
performing an operation or command representative of said analyzed digital signal by
said processor.

Claim 34 (canceled)

Sincerely,

Christopher M. Tucker
Agent for Applicant